The performances of R GPU implementations of the GMRES method

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ABSTRACT

Although the performance of commodity computers has improved drastically with the introduction of multicore processors and GPU computing, the standard R distribution is still based on single-threaded model of computation, using only a small fraction of the computational power available now for most desktops and laptops. Modern statistical software packages rely on high performance implementations of the linear algebra routines there are at the core of several important leading edge statistical methods. In this paper we present a GPU implementation of the GMRES iterative method for solving linear systems. We compare the performance of this implementation with a pure single threaded version of the CPU. We also investigate the performance of our implementation using different GPU packages available now for R such as gmatrix, gputools or gpuR which are based on CUDA or OpenCL frameworks.

Keywords: R; GPU; statistical software; GMRES

JEL Classification: C6, C8

1. INTRODUCTION

Since the appearance of the first supercomputers in early ‘70s the nature of parallel computing has changed drastically. Besides the advancements in technology that allow building processors with a very high clock rate and a low specific dimension, today the commodity computers are based on multicore architecture that can run multiple tasks in parallel. Besides the classical multiheaded programming, another parallel computing paradigm called GP-GPU computing (general-purpose computing on graphics processing units), first experimented around 2000-2001 (see for example Larsen and McAllister, 2001), is widely used nowadays to speed up numerical intensive applications.

While numerical intensive parts of applications are handled
traditionally by the CPUs, now GPUs have FLOP rates much higher than CPUs because GPUs are specialized for highly parallel intensive computations and they are designed with much more transistors allocated to data processing rather than control flow or data caching. This is shown in figure 1 where it can be noticed that the total surface of the device dedicated to ALUs is much higher in case of GPU than CPU.

The theoretical FLOP rates for GPUs and CPUs are presented in figure 2 and the memory bandwidth in figure 3. Current GPUs have FLOP rates at least 100 times greater than CPUs and the memory bandwidth is at least 10 times greater for GPU memories than for the main memory of the computer. These figures indicate that GPUs are ideal candidates to dispatch the numerical parts of applications.

A comparison between CPU and GPU structure

Figure 1

Statistical software relies heavily on numerical computations and can take advantage of the new paradigm of GPU computing. In this paper we will evaluate the performances of an R implementation of a numerical algorithm widely used in linear algebra libraries, namely the GMRES method to solve linear systems (Saad and Schultz, 1986). The rest of the paper is structured like this: in section 2 we briefly present the GPU computing frameworks currently available for scientific computing, in section 3 we present the GMRES algorithm, in section 4 we present some R packages that implements the GPU-GPU computing model and discuss three R implementations that uses GPU computing, comparing their performances with the serial implementations. Section 5 concludes our paper.

A comparison of the theoretical FLOP rates of typical CPUs and GPUs\textsuperscript{1}

\begin{figure}[h]
\centering
\includegraphics[width=\textwidth]{Figure_2}
\caption{A comparison of the theoretical FLOP rates of typical CPUs and GPUs.}
\end{figure}

\begin{figure}[h]
\centering
\includegraphics[width=\textwidth]{Figure_3}
\caption{A comparison of the memory bandwidth for CPU and GPU\textsuperscript{2}.}
\end{figure}

2. GPU COMPUTING FRAMEWORKS

GPUs are designed to solve general problems that can be formulated as data-parallel computations: the same stream of instructions is executed in parallel on many (different) data elements with a high ratio between arithmetic operations and memory accesses, like the SIMD approach of the parallel computers taxonomy. Currently there are several frameworks that implement GP-GPU model:

- CUDA (Compute Unified Device Architecture) – first introduced in 2006 by NVIDIA, is a general purpose parallel programming architecture that uses the parallel compute engines in NVIDIA GPUs to solve numerical intensive problems in a more efficient way than a CPU does (NVIDIA Corporation, 2007);

- OpenCL (Open Computing Language) - is a framework for writing programs that execute across heterogeneous platforms consisting of CPUs, GPUs, digital signal processors (DSPs), field-programmable gate arrays (FPGAs) and other hardware accelerators (OpenCL Specification Version 1.0, 2008);

- OpenACC - (Open Accelerators) is a programming standard for parallel computing developed by Cray, CAPS, NVIDIA and PGI. The standard is designed to simplify parallel programming of heterogeneous CPU/GPU systems – it works on NVIDIA, AMD and Intel accelerators (OpenAcc-Standard.org, 2017);

In this paper we used CUDA framework and the R packages that allows access to the CUDA API. CUDA parallel programming framework works with three important abstractions (Oancea et al., 2012a):

- a hierarchy of thread groups;
- shared memories;
- a barrier synchronization accessible by the programmer;

CUDA parallel programming paradigm requires programmers to (NVIDIA Corporation, 2017):

- partition the problem into coarse tasks that can be run in parallel by blocks of threads;
- divide each task into finer groups of instructions that can be executed cooperatively in parallel by the threads within a block;

The main programming language for CUDA is C but nowadays there are bindings to other languages too: Java, Fortran, Common Lisp, Python, R. While C libraries that uses CUDA for numerical computing are mature now,
the bindings to other languages are only at the beginning. In (Oancea et al., 2012b) we investigated the performance of a Java library that uses CUDA for numerical computations and found that one can achieve an important speedup compared to the classical (serial) version of the respective algorithm while in (Oancea and Andrei, 2013) we investigated a hybrid approach CPU-GPU. In this paper we will test if a linear algebra algorithm implemented in R is capable to obtain a significant speedup compared with a serial R implementation.

CUDA paradigm of GPU computing

Figure 4

3. GMRES METHOD

One question may arise: why GMRES? When using GPU for numerical computations the biggest speedups are obtained by algorithms that use level 3 BLAS operations (such as matrix-matrix multiplication). GMRES

1 http://docs.nvidia.com/cuda/cuda-c-programming-guide/
uses only level 1 and level 2 operations (vector updates and matrix-vector products) and we wanted to test if the available GPU R packages can obtain an important advantage (speedup) over classical numerical algorithms even for methods that are not easily parallelizable.

The GMRES (Generalized Minimum RESidual) was first proposed in 1986 as a Krylov subspace method for nonsymmetric systems (Saad and Schultz, 1986). This method approximates the solution by a vector in a Krylov subspace with minimal residual – this vector is build using the so-called Arnoldi iteration.

The problem to be solved is $Ax = b$. The n-th Krylov subspace for this problem is: $K_n = K_n(A, b) = \text{span}\{b, Ab, A^2b, \ldots, A^{n-1}b\}$. GMRES approximates the exact solution of the linear system with by the vector $x_n \in K_n$ that minimizes the Euclidean norm of $r_n = Ax_n - b$. The restarted version of the GMRES algorithm that we implemented is presented below (Kelley, 1995):

1. choose $x_0$ and compute $r_0 = b - Ax_0$ and $v_1 = r_0 / \| r_0 \|
2. for j = 1, 2, \ldots, m$ do
3. \hline
4. $h_{ij} = (Av_i, v_j)$ for $i = 1, 2, \ldots, j$
5. $\tilde{v}_{j+1} = Av_j - \sum_{i=1}^{j} h_{ij}v_i$
6. $h_{j+1,j} = \| \tilde{v}_{j+1} \|
7. $v_{j+1} = \tilde{v}_{j+1} / h_{j+1,j}$
8. endfor
9. compute $x_m = x_0 + V_m y_m$ where $y_m$ minimizes $\| \beta e_1 - \tilde{H}_m y \|$ for $y \in \mathbb{R}^m$
10. Restart – compute $r_m = b - Ax_m$
11. if $r_m < \varepsilon$ stop
12. else $x_0 = x_m, v_1 = r_m / \| r_m \|$ and go to line 2 (for) (restart iterations)

This algorithm contains a level 2 BLAS operation, namely the matrix-vector product $Av_i$ that has to be evaluated at each iteration and level 1 BLAS operations – vector updates inside the for loop. The least squares problem (8) can be solved maintaining a QR factorization of $H$ and it requires $O(mN)$ floating point operations (Kelley, 1995). There are several versions of the GMRES algorithms presented in the literature that work at a block level to improve the efficiency or use techniques to parallelize the computations (Chronopoulos, 1986), (Chronopoulos and Kim, 1992), (Chronopoulos and Swanson, 1996), (Chronopoulos and Kucherov, 2010), (Ranjan et al. 2016). In this paper we focused on using GPU
4. R SUPPORT FOR GPU COMPUTING

Currently there are several R packages that support GPU/CUDA framework: gmatrix (Moris, 2016), gpuR (Determan, 2017), gputools (Buckner and Wilson, 2013), cudaBayesreg (da Silva, 2011), HiPLARb (Szeremi, 2012), HiPLARM (Nash and Szeremi, 2012) Rcula (Morris, 2013). Some of these packages work only with CUDA while others also support OpenCL or OpenACC. While some packages are designed to solve a single type of problems, others are general purpose packages that allow users to implement different types of algorithms. From this latter category we chose gmatrix, gpuR and gputools and implemented the GMRES iterative algorithm for solving linear systems to evaluate the performances of the current R packages that uses the GPU computing model.

gmatrix is a general-purpose package that works with only NVIDIA CUDA toolkit. It has gmatrix() and gvector() methods that allow users to create matrices and vectors on GPU device and all the computations with these matrices/vectors are performed by default on GPU. Data can be transferred between the host memory (RAM) and device memory (GPU) using two simple methods: h() and g(). This package implements several numerical operations with matrices and vectors on the GPU such as matrix multiplication, addition, subtraction, the Kronecker product, the outer product, comparison operators, logical operators, trigonometric functions, indexing, sorting, random number generation, etc. A matrix or a vector on device memory can be created as simple as:

```r
> A <- gmatrix(1:400, 20, 20)
> g_seq <- gseq(1:20)
> b <- as.gvector(g_seq)
```

Performing algebraic operations with such objects is very simple since most of the arithmetic and logical operators (+, -, *, /, %%, ^, ==, !=, <, >, <=, >=, &|) are overloaded:

```r
> c <- A %**% b
```
In our implementation of the GMRES algorithm using \textit{gmatrix} package we performed only the matrix-vector product on GPU while the rest of the operations are performed by the CPU. The reason why we chose to send to the GPU only the matrix-vector product is that the level 1 operations start to have a speedup > 1 only for very large vectors (N>5e5) according to (Morris, 2016) and due to GPU memory limitations, we used N between 1e3 and 1e4 which is considerably lower than the threshold where the speedup is greater than one.

\textit{gputools} is another general-purpose package that works with CUDA toolkit. Matrices and vectors are created on the host memory using the normal methods and then they are transferred to the device memory where computations took place. After the matrices and vectors are created, the matrix operations dispatched to the GPU were performed using \textit{gpuMatMult}. Again, we implemented only level 2 operations on GPU because level 1 operations would not be efficient to send to the GPU due to the high overhead incurred by the memory transfers.

Below is a simple example of how a matrix-vector product can be send for execution to the GPU:

\begin{verbatim}
> N <- 200
> A <- matrix(rnorm(N * N), nrow = N)
> B <- matrix(rnorm(N), nrow = N)
> C <- gpuMatMult(A, B)
\end{verbatim}

\textit{gpuR} is the third package that we tested, and it works both with CUDA and OpenCL backends. It has \textit{gpuMatrix()} and \textit{gpuVector()} methods that allow users to create objects on the host memory but the computations with these objects are performed on GPU in a transparent way. There are also two other methods \textit{vclMatrix()} and \textit{vclVector()} that creates the respective objects directly into the device memory and work asynchronously. By using the asynchronous mode, R will immediately return to the CPU after calling any operation that involves \textit{vcl} objects. When one tries to access data computed using \textit{vcl} objects, but the actual computations done by the GPU are not ready, R will wait until the computations are finished and the data become available.

Below are examples of how some basic matrix operations look like using \textit{gpuR}:

\begin{verbatim}
> N <- 200
> A <- matrix(rnorm(N * N), nrow = N)
> B <- matrix(rnorm(N), nrow = N)
> gpuA <- gpuMatrix(A, type = "double")
> gpuB <- gpuMatrix(B, type = "double")
> gpuC <- gpuA %*% gpuB
\end{verbatim}
The most commonly used operators are overloaded: %*%, +, -, *, /, crossprod, tcrossprod, etc. to work with GPU objects. For GMRES we implemented all numerical operations on GPU using vccl objects and methods: this approach speeds up the computation but put a limit through the available GPU memory.

The experimental setup consists of a computer with:
- Intel core i7 4710HQ processor at 2.5GHz;
- 6 MB level 3 cache, 1 MB level 2 cache and 256 KB level 1 cache;
- 16 GB DDR3 RAM;
- NVIDIA GeForce 840M graphic card (it has a Maxwell architecture);
- 2 GB video RAM with a bandwidth of 16 GB/s;
- 384 shader units clocked at 1029 MHz;
- Ubuntu ver. 16.04;
- CUDA framework ver. 8.0.61;
- R ver. 3.2.3;
- gmatrix ver. 0.3, gputools ver. 1.1, gpuR ver. 1.2.1;

The following table shows the speedup of the GPU implementations compared with a serial algorithm (gmres() from pracma package). We used matrices with dimensions between 1000 and 10000. The size of the problem was limited by the available amount of the graphics card memory. It can be observed that the speedups are rather modest, only gpuR showing a speedup between 3 and 4. Increasing the problem size could lead to a better speedup but the limited amount of memory on the graphics card precluded us to use bigger matrices.

Running times for different implementations and different size of the problem

<table>
<thead>
<tr>
<th>N</th>
<th>gmatrix implementation</th>
<th>gputools implementation</th>
<th>gpuR implementation</th>
</tr>
</thead>
<tbody>
<tr>
<td>1000</td>
<td>1.06</td>
<td>0.75</td>
<td>0.99</td>
</tr>
<tr>
<td>2000</td>
<td>1.28</td>
<td>0.77</td>
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<tr>
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<tr>
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</tr>
<tr>
<td>5000</td>
<td>1.36</td>
<td>1.04</td>
<td>2.33</td>
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</table>
5. CONCLUSIONS

GPU computing becomes an important solution for numerical computing. While C libraries are mature and deliver high speedups, R packages that implements GPU computing are at the beginning. For the three packages that we tested we obtained moderate speedups (~3…4) compared with the serial version of the algorithm. These low speedups are due to the limited amount of device memory that precluded us to use bigger matrices. It is shown in (Morris, 2016) that for level 1 BLAS operations the size of the vector should be greater than 5E5 to obtain speedups greater than 1, but such dimensions are too high for our version of GMRES to fit into the device memory. For commodity computers, where the available amount of graphics card memory is limited the speedups are low and they are comparable with speedups obtained by using a tuned linear algebra library (Oancea et al., 2015). Nevertheless, using GP-GPU together with specialized R packages could be a solution for the future.
References